Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) An ~~information processing~~ apparatus comprising:

a drive device;

a communication bus;

a first processor coupled to the communication bus, the first processor to decode a first stream data routed over the communication bus;

a second processor provided with a second stream data received from the drive device without being routed over the communication bus, the second processor to decode the second stream data to reproduce the second stream data in accordance with an instruction sent from the first processor over the communication bus;

~~an application program, under control of the first processor, issuing a processing execution request to the second processor;~~

~~an actual processing section, under control of the second processor, executing an application interface processing code defined by the second processor; and~~

~~an interface that, when an execution request of the application interface processing is issued by the application program, sends the processing execution request to the actual processing section through a communication bus~~.

2. (Currently Amended) The ~~information processing~~ apparatus according to claim 1, wherein the second processor is a stream processor.

3. (Currently Amended) The ~~information processing~~ apparatus according to claim 1, wherein the first processor is a central processing unit (CPU).

4. (Cancelled).

5-7. (Cancelled).

8. (Withdrawn) An information processing apparatus comprising:

a first processor;

a communication bus;

a bridge device coupled between the first processor and the communication bus;

a second processor coupled to the communication bus, the second processor processing stream data; and

control logic coupled to the communication bus and which, when a power-on signal is detected, issues a reset signal to each of the second processor and the first processor through the communication bus and issues a reset release signal to the first processor after issuing a reset release signal to the second processor.

9-17. (Cancelled).

18 (New) The apparatus according to claim 1, wherein the drive device is a hard disk drive.

19. (New) The apparatus according to claim 1 further comprising:

a network control unit coupled to the communication bus, the network control unit to transmit the first stream data via the communication bus.

20. (New) The apparatus according to claim 1, wherein the network control unit includes an IEEE 1394 processor.

21. (New) The apparatus according to claim 1, wherein the second stream data includes video data and audio data.

22. (New) The apparatus according to claim 1, wherein the communication bus is a Peripheral Component Internet (PCI) bus.

23. (New) The apparatus according to claim 1, further comprising:

a video bus; and

a graphic controller in communication with the first processor and the second processor, the graphic controller to convert the decoded first stream data into display video signals and to transmit the display video signals to the second processor over the video bus.

24. (New) The apparatus according to claim 23, wherein the second processor superposes the display video signals transmitted over the video bus on a video image generated from the decoded second stream data in accordance with display information transferred from the first processor to the second processor over the communication bus.

25. (New) The apparatus according to claim 24, wherein the display information includes information designating a region in a drawing area and a transparency rate at the display video signals on a screen.

26. (New) The apparatus according to claim 1, further comprising:

a television tuner adapted to transmit a third stream data to the second processor for storage into a storage medium associated with the drive device.

27. (New) The apparatus according to claim 1, further comprising:

a television tuner; and

a transport stream bus coupled to the television tuner and the second processor, the transport stream bus enables transmission of the third stream data to the second processor without using the communication bus.

28. (New) An apparatus comprising:

a communication bus;

a drive device;

a video terminal;

a first processor coupled to the communication bus, the first processor to decode a first stream data sent over the communication bus; and

a second processor coupled to the drive device, the video terminal and the first processor, the second processor being provided with a second stream data sent from the drive device without use the communication bus, the second processor to (i) decode the second stream data

for reproducing the second stream data in accordance with an instruction sent from the first processor via the communication bus and (ii) display video signals, that are based on the decoded first stream data and transmitted by the first processor over a video bus separate from the communication bus, on the video terminal.

29. (New) The apparatus according to claim 28, wherein the second processor superposes the display video signals on a video image generated from the decoded second stream data in accordance with display information transferred from the first processor to the second processor through the communication bus.

30. (New) The apparatus according to claim 29, wherein the display information includes information designating a region in a drawing area and a transparency rate at the display video signals on a screen.